

**AMENDMENTS TO THE CLAIMS**

**Claim 1** (Original) An in-plane switch mode liquid crystal display device comprising:

a plurality of data lines for applying data signals to a thin film transistor array;

a plurality of gate lines for applying gate signals to the thin film transistor array;

a plurality of common voltage lines for applying a common voltage to the thin film transistor array; and

a plurality of dummy signal lines parallel to the common voltage lines and adjacent to the common voltage lines for applying alternating current signals.

**Claim 2** (Original) The liquid crystal display device according to claim 1, wherein the dummy signal lines are supplied with alternating current signals whose polarity is inverted for each frame.

**Claim 3** (Original) The liquid crystal display device according to claim 1, wherein a plurality of common voltage pads for applying a common voltage signal from an external driving circuit to the common voltage lines are formed in a data pad area provided with data pads connected to the data lines and in gate pad area provided with gate pads connected to the gate lines.

**Claim 4** (Original) The liquid crystal display device according to claim 1, wherein a plurality of dummy signal pads for applying alternating current signals from an external driving circuit to the dummy signal lines are formed in a data pad area provided with data pads connected to the data lines.

**Claim 5** (Original) The liquid crystal display device according to claim 1, wherein a plurality of dummy signal pads for applying alternating current signals from an external driving

circuit to the dummy signal lines are formed in a gate pad area provided with gate pads connected to the gate lines.

Claim 6 (Currently Amended) An in-plane switching mode liquid crystal display device comprising:

a plurality of data lines for applying data signals to a thin film transistor array;  
a plurality of gate lines for applying gate signals to the thin film transistor array; and  
a plurality of common voltage lines for applying a common voltage to the thin film transistor array, wherein the common voltage lines provided in an outer area of the thin film transistor array are spaced from the thin film transistor array by a distance greater than or equal to 1 mm to prevent deterioration of liquid crystal generated in said outer area from being diffused into the thin film transistor array.

Claim 7 (Cancelled)

Claim 8 (Previously Presented) The in-plane switching liquid crystal display device according to claim 6, wherein the distance is in a range of equal to or greater than 1mm and less than or equal to 1.5mm.

Claim 9 (Previously Presented) The in-plane switching liquid crystal display device according to claim 6, wherein the distance is greater than 1.5mm.

Claim 10 (Original) An in-plane switching mode liquid crystal display device, comprising:

a plurality of data lines for applying data signals to a thin film transistor array;  
a plurality of gate lines for applying gate signals to the thin film transistor array;  
a plurality of common voltage lines for applying a common voltage to the thin film transistor array; and

at least one dummy data line, parallel to said data lines, for applying a compensation signal;

wherein the common voltage lines are provided outside the thin film transistor array, and wherein the common voltage lines are spaced a predetermined distance from the thin film transistor array.

Claim 11 (Original) The in-plane switching mode liquid crystal display device of claim 10, wherein the dummy data line is outside the data lines of the thin film transistor array.

Claim 12 (Original) The in-plane switching mode liquid crystal display device of claim 10, wherein the dummy data line is between the common voltage lines and the data lines.

Claim 13 (Original) The in-plane switching mode liquid crystal display device of claim 10, wherein the at least one dummy data lines includes a first dummy data line, adjacent to and parallel to the common voltage line.

Claim 14 (Original) The in-plane switching mode liquid crystal display device of claim 13, wherein the first dummy data is between the common voltage line and the thin film transistor array.

Claim 15 (Original) The in-plane switching mode liquid crystal display device of claim 13, wherein the at least one dummy data lines includes a second dummy data line, adjacent to and parallel to the common voltage line.

Claim 16 (Original) The in-plane switching mode liquid crystal display device of claim 15, wherein the second dummy data line is on a side of the common voltage line opposite the thin film transistor array.

Claim 17 (Original) The in-plane switching mode liquid crystal display device of claim 10, wherein the compensation signal has a low voltage and a high voltage, wherein the low voltage is equal to the common voltage minus a predetermined voltage and the high voltage is equal to the common voltage plus the predetermined voltage.

Claim 18 (Original) The in-plane switching mode liquid crystal display device of claim 17, wherein the low voltage is 0 and the high voltage is 10V.

Claim 19 (Original) The in-plane switching mode liquid crystal display device of claim 10, wherein the compensation signal varies according to the common voltage such that the compensation signal has an average voltage equal to the common voltage.

Claim 20 (Original) The in-plane switching liquid crystal display of claim 10, wherein the compensation signal is an alternating signal centered on the common voltage.